

aligning the output single-ended clock signal with the received clock signal.

²¹/₂₅. (Amended) The method of claim ²⁰/₂₄ wherein aligning includes compensating for the processed clock signal time delay.

²²/₂₆. (Amended) A system comprising:
a clock generator, wherein the clock generator issues one of a single-ended clock signal or a differential clock signal;
and
an electronic device including a first input terminal and a second input terminal, with the first input terminal coupled to the clock generator, the electronic device to generate a single-ended clock signal of the same frequency as the clock signal issued by the clock generator and aligned with the clock signal issued by the clock generator.

²⁴/₂₇. (Amended) The system of claim ²³/₂₆, wherein the electronic device includes a phase lock loop to compensate for delays in processing the clock generator clock signal so that the electronic device single-ended clock signal is aligned with the clock generator clock signal.

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²⁵/₂₈. (Amended) The system of claim ²³/₂₆,

wherein the electronic device couples the first input terminal to circuit ground when the clock generator issues a single-ended clock signal.

²⁶/₂₉. (Amended) The system of claim ²³/₂₆,

wherein the electronic device first and second input terminals are coupled to the clock generator when the clock generator issues ^{the} a differential clock signal.--

Please add the following new claim:

²⁷/₃₀. (New) The method of claim ²¹/₂₈ wherein compensating

includes providing adjustable feedback as a function of whether the received clock signal is the single-ended clock signal or the differential clock signal.